

LSI LOGIC

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TC 2858

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## REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please comment

Application Number: 10/719,878  
 Filing date: November 21, 2003  
 First named inventor: Jeff Brown et al.  
 Attorney docket number: 03-0940 / 5201-27900

Transmitted herewith for filing via facsimile:

- Information Disclosure Statement Letter
- Information Disclosure Statement by Applicant PTO/SB/O&A

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

Date

4/13/04

Signature

Connie del Castillo

Connie del Castillo

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of :**

**Jeff Brown et. al.**

**Serial No. :** 10/719,878

**Filed :** November 21, 2003

**For :** Device And Method For Using A Lessened Load To Measure Signal Skew At The Output Of An Integrated Circuit

www.pearsoned.com

**Group Art Unit :** 2858

**Examiner :**

**Attv Docket :** 5201-27900 / 03-0940

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

4/23/04 *Connie del Castillo*  
Date Signature

## **INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation  
1551 McCarthy Blvd., MS D-106  
Milpitas, CA 95035  
408-433-7475

Date: 4/13/04

Respectfully submitted,

Leo Peters

Reg. No. 33,562



PTO/SB/08A (10-96)

Approved for use through 10/31/99, OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

## **Complete if Known**

Application Number	10/719,878
Filing Date	November-21, 2003
First Named Inventor	Jeff Brown
Group Art Unit	2858
Examiner Name	
Attorney Docket No.	5201-27900 / 03-0940

Sheet 1 of 1

## **U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code (if known)			
		5,991,890		Brown et al.	11-23-1999	
		4,929,889		Seiler et al.	05-29-1990	
		5,289,477		Lenta et al.	02-22-1994	

Examiner signature		Date considered	
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Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office.